

1/4

Fig. 1
PRIOR ART

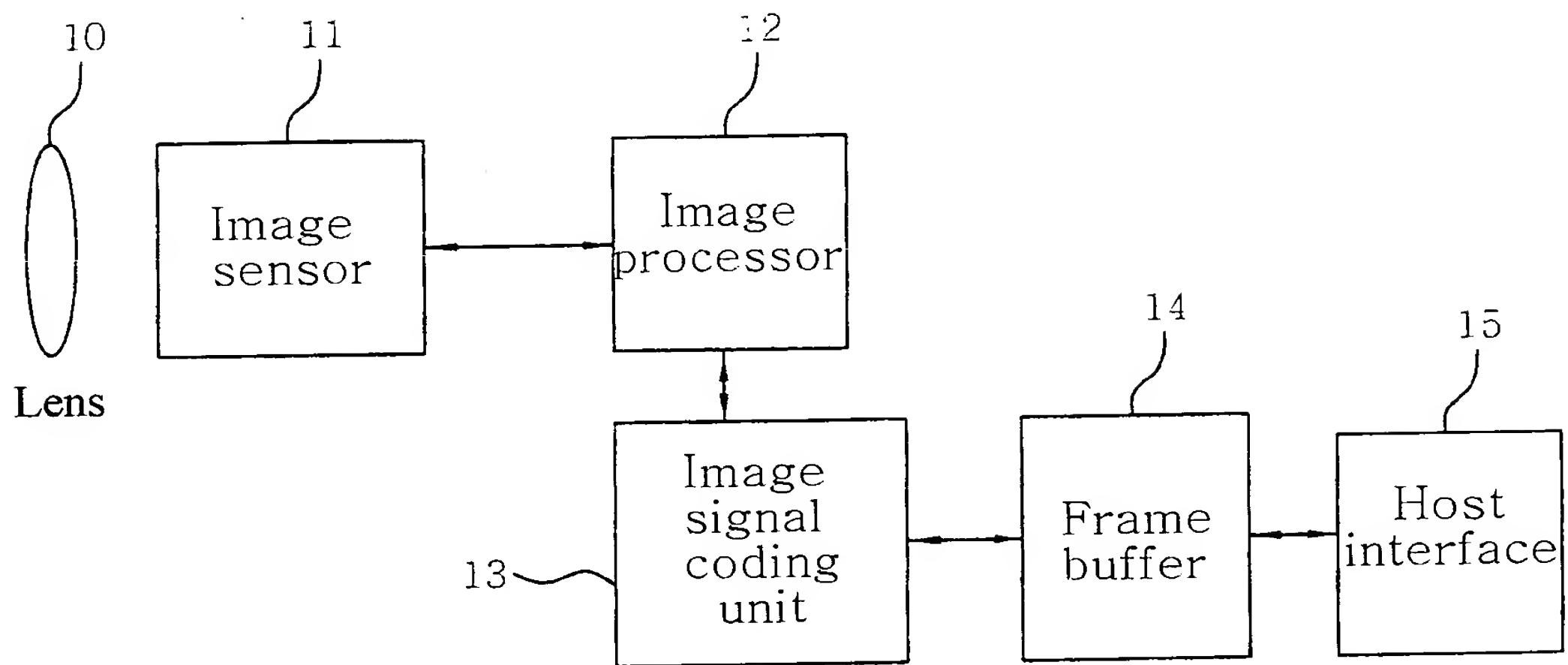
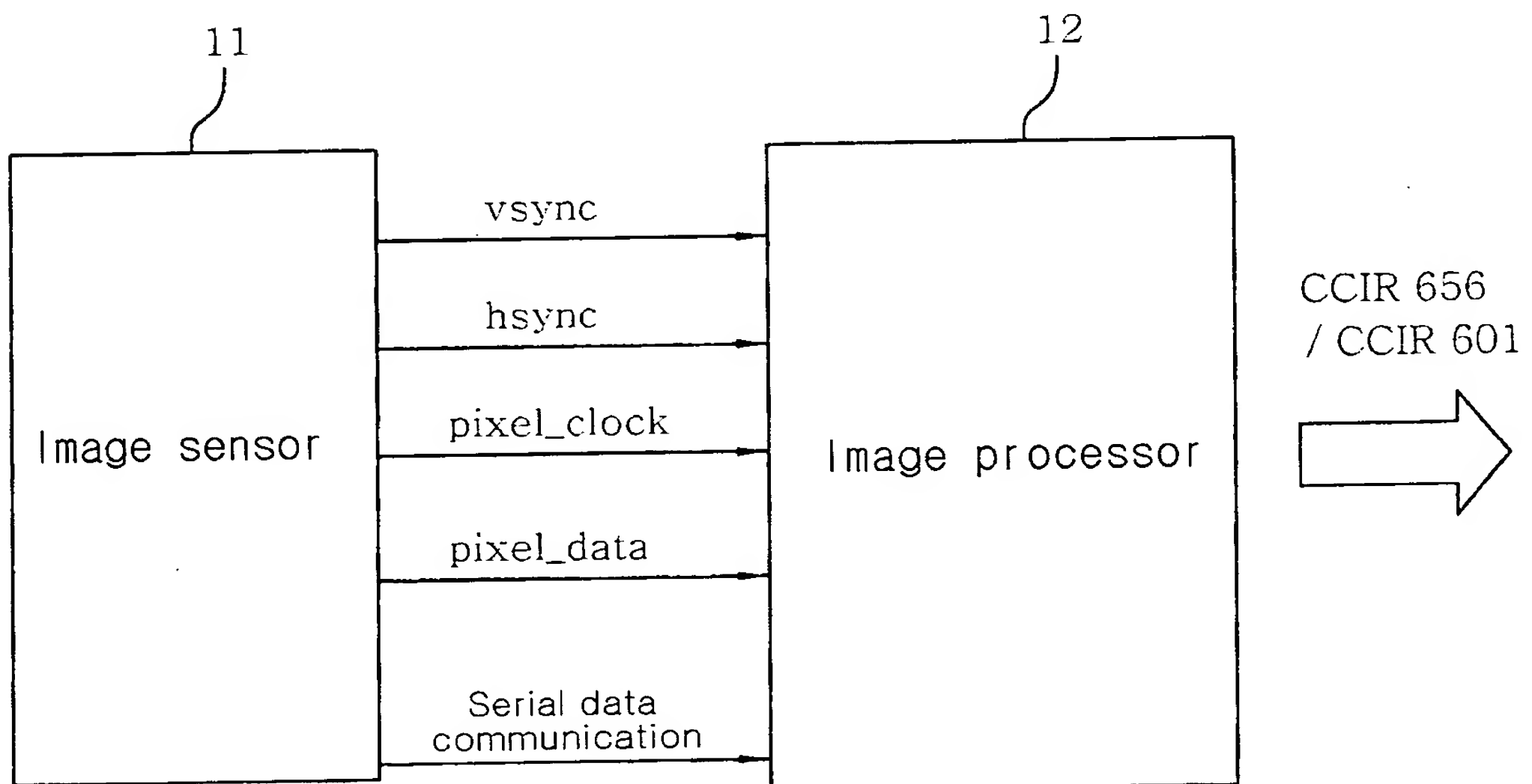


Fig. 2
PRIOR ART



2/4

Fig. 3

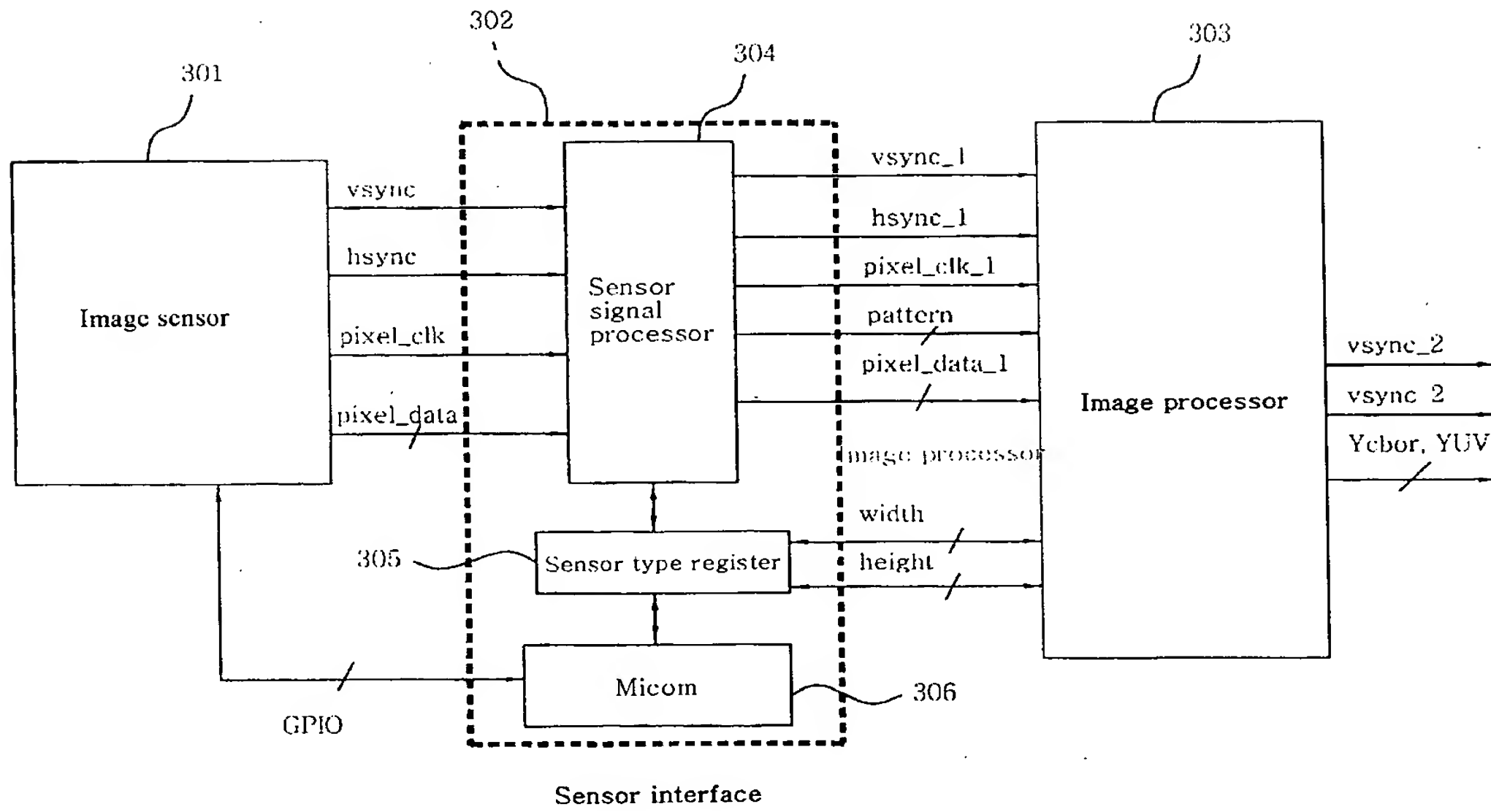
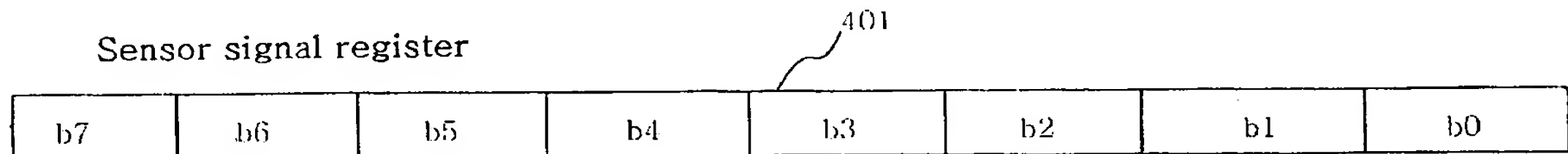


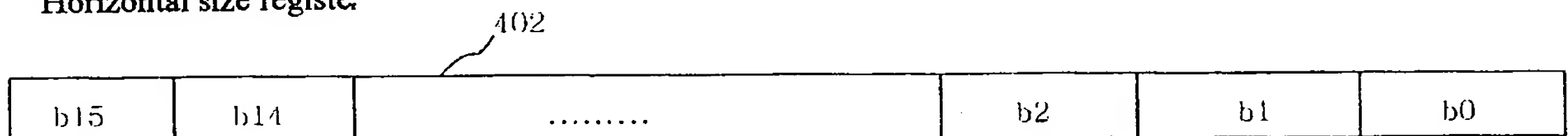
Fig. 4a



- [b1: b0] : Interpolation pattern
- b2: : Polarity of pixel clock signal
- b3: : Polarity of vertical synchronization signal vsync
- b4: : Polarity of horizontal synchronization signal hsync
- b5: : ISP mode
- [b7: b6] : not assigned

Fig. 4b

Horizontal size register.



3/4

Fig. 4C

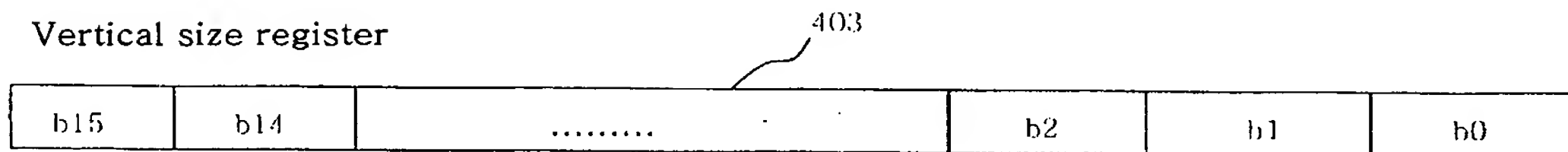
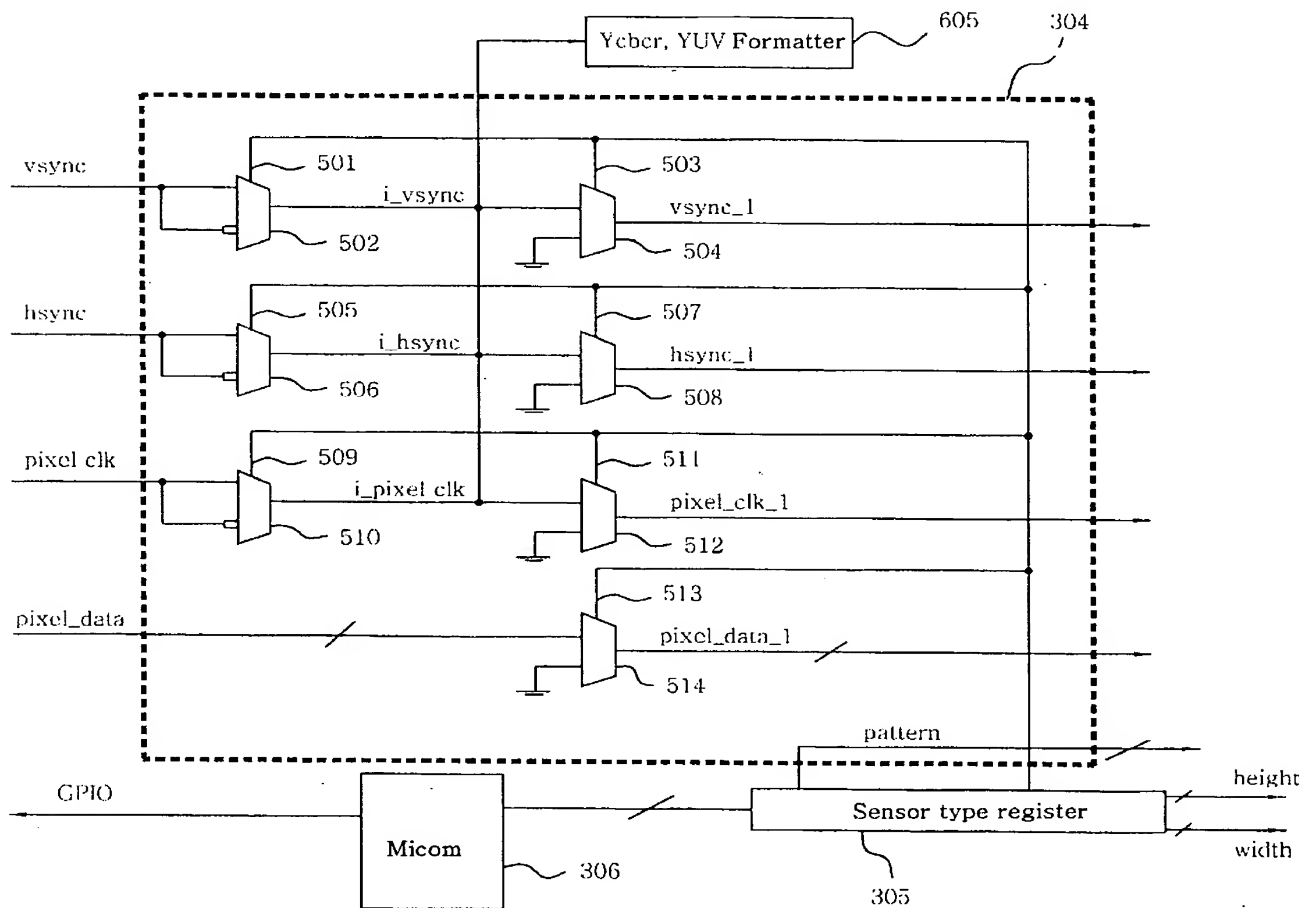


Fig. 5



4/4

Fig. 6

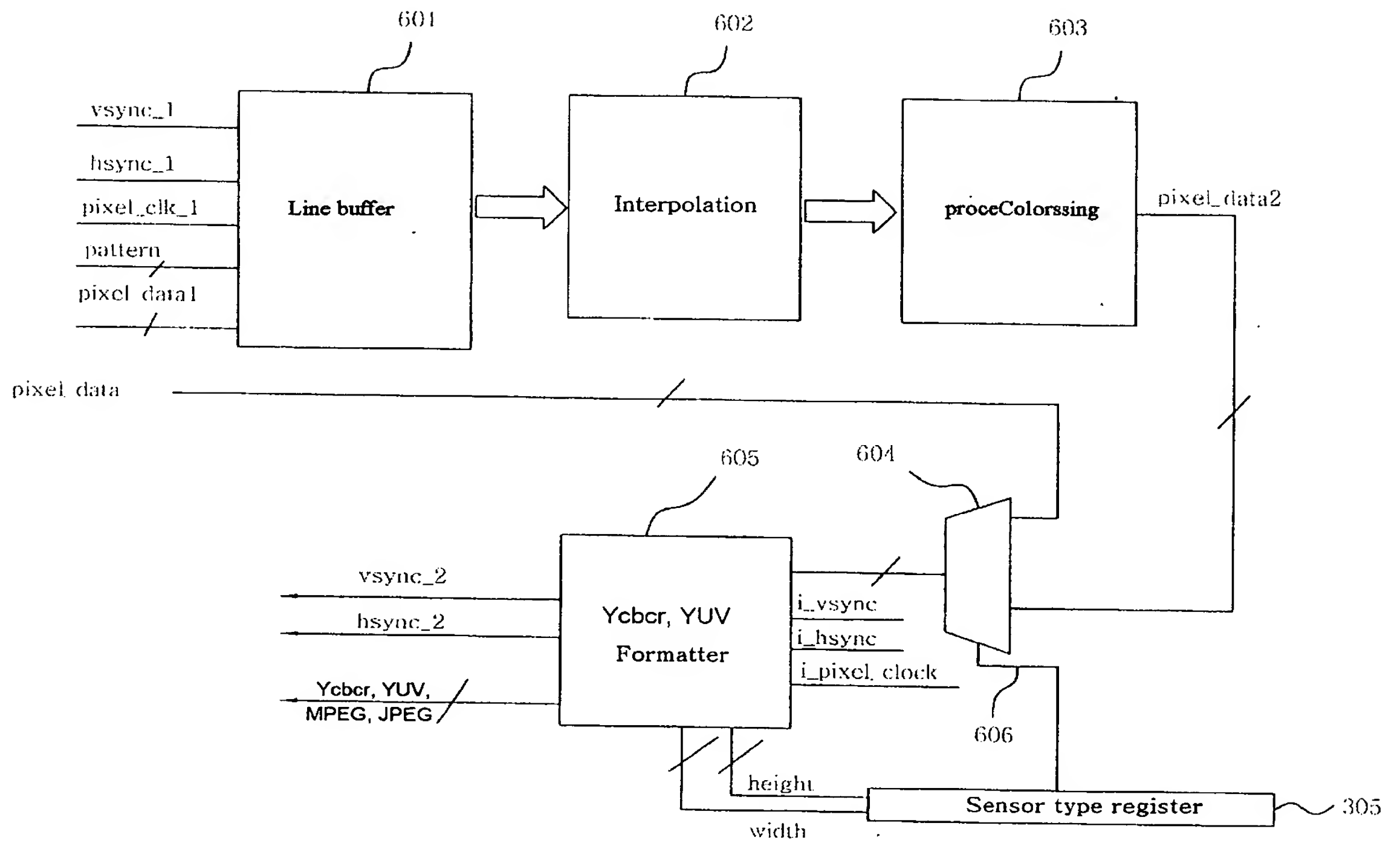


Fig. 7

| Sensor | Vsync | Hsync | Pclk | Data Format | Size | Resolution | Active Pixel size | Control method |
|----------------------|---------|------------|---------|---------------|------------|------------|-------------------|--------------------|
| AU-60A(Hynics) | high | high | posedge | CbYCrY | 1/4 inch | VGA | 652x488 | I2C |
| TCM8210MD(Toshiba) | high | high | negedge | UYVY | 1/4 inch | VGA | 660x492 | I2C |
| OV7648 (Omnivision) | low | high | posedge | UYVY | 1/4 inch | VGA | 640x480 | Serial SCCB |
| OV7635 (Omnivision) | low | high | posedge | UYVY | 1/4 inch | VGA | 640x480 | Serial SCCB |
| HV7131G (Hynics) | low | high | posedge | BGBG ... GRGR | 1/4 inch | VGA | 652x488 | I2C |
| PVO430DC (Pixelplus) | low | high | negedge | UYVY | 1/4,5 inch | VGA | 640x480 | I2C |
| ZR32312(Zoran) | high | high | posedge | BGBG...GRGR | 1/3,4 inch | SXGA | 1288x968 | I2C(SMBUS) |
| Bi8831 (Biomorphic) | high | high | posedge | GRGR... BGBG | 1/3 inch | SXGA | 1296x1040 | 3-wire serial int. |
| LZOP3721(Sharp) | high | high | posedge | UYVY | 1/4 inch | SXGA | 1172x896 | I2C |
| OV9620(OmniVision) | low | high | negedge | BGBG... GRGR | 1/2 inch | SXGA | 1280x1024 | Serial SCCB |
| MCM20027(Motorola) | posedge | vclock low | posedge | GRGR... BGBG | 1/2 inch | SXGA | 1280x1024 | I2C |
| PT20503 (PICTOS) | high | high | posedge | GRGR... BGBG | | SXGA | 1280x920 | 2wire serial int. |
| PO1130K | low | high | negedge | UYVY(CbYCrY) | | SXGA | 1280x1024 | I2C |